Application No.: 10/797,108

AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims

1-3 (Cancelled)

4. (Currently Amended) The display of claim 1, wherein A display comprising:

a display panel provided with scanning lines, signal lines located to intersect the scanning lines, and sub-pixels connected to the signal lines;

a source driver, whose output terminals are each connected to an associated one of the signal lines, for driving the sub-pixels; and

a controller for supplying a control signal to the source driver,

wherein given that n is an integer of two or more, the polarity of an output voltage supplied from each output terminal is switched relative to a common voltage in every n horizontal scanning periods, and the timing of switching of the polarity of the output voltage is shifted by one horizontal scanning period for each frame,

the source driver has a polarity shift circuit to which a polarity switching signal for controlling the switching of the polarity of the output voltage is inputted, and which outputs the polarity switching signal by shifting the signal by one horizontal scanning period for each frame, and

the source driver further has electrical charge recovering means that is provided between two of the output terminals, and is controlled so as to short-circuit which controls such that at

Application No.: 10/797,108

least the two output terminals <u>are short-circuited</u> for a certain period of time in n horizontal scanning periods in reaction to the output of the polarity shift circuit.

5-6. (Cancelled)

7. (Currently Amended) The method of claim 5, wherein A method for driving a display comprising: a display panel having scanning lines, signal lines located to intersect the scanning lines, and sub-pixels that are connected to the signal lines and arranged in a matrix pattern: and a source driver, whose output terminals are each connected to an associated one of the signal lines, for driving the sub-pixels, the source driver further has having electrical charge recovering means provided between two of the output terminals, the display being driven by employing an n line dot inversion drive scheme given that n is an interger of two or more,

wherein the method comprises the steps of:

a) supplying, from each output terminal of the source driver, an output voltage whose polarity is switched for every n lines;

b) shifting the timing of switching of the polarity of the output voltage from each output terminal line by line for each frame; and

wherein c) given that n horizontal scanning periods are defined as one cycle, the method further comprises the step of controlling the electrical charge recovering means, based on the timing of switching of the shifted polarity, so that at least the two output terminals are short-circuited for a certain period of time when the polarities of the two output terminals are both switched.

Application No.: 10/797,108

8-9. (Cancelled)